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JSS Mahavidyapeetha

JSS Science And Technology University

(Established Under JSS Science and Technology University Act No. 43 of 2013)

(Formerly Known as SJCE)

JSS SCIENCE AND TECHNOLOGY UNIVERSITY MYSURU

DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ACADEMIC YEAR 2018-19 (EVEN SEMESTER) – PG

Semester: M. Tech **Section: Industrial Electronics** **Room. No.: AB102**

Day	7.30 – 8.30	8.30 – 9.30	9.30 – 10.30	10.30 – 11.00	11.00 – 11.50	11.50 – 12.40	12.40 – 1.30	1.30 – 2.30	2.30 – 3.30	3.30 – 4.30	4.30 – 5.30
Mon			LIE210	BREAK	LIE230	LIE242	PGOL8	BREAK	LIE26L (DSP LAB)		
Tue			LIE242		LIE210	PGOL8	LIE220		LIE27S (Power Electronics Lab)		
Wed			LIE230		LIE220	LIE242 (Tutorials)					
Thu		LIE210	LIE220		LIE230	LIE242	PGOL8		LIE230 – L (Automotive Electronics Lab)		
Fri			LIE220		PGOL8	LIE210 (Tutorials)			LIE220 (Tutorials)		
Sat		LIE242	LIE210		PGOL8 (Tutorials)		LIE230				

Sub-Code	Subject Title	Credits	Course Instructor	Initials
LIE210	Digital control systems	5	Dr. Sudarshan Patil Kulkarni	SPK
LIE220	Robotics and Industrial automation	5	Ramya M V	RMV
LIE230	Automotive Electronics	5	B A Sujathakumari	BASK
LIE242	Testing and Verification of VLSI circuits	5	Dr. S Gayathri	SG
PGOL8	MEMS	5	Dr. M G Veena	MGV
LIE26L	IE Lab-2	1.5	Yashwanth S D	YSD
LIE27S	Design and implementation -2	1.5	Dr. S Gayathri	SG
Total Credits		28		

Head of the Dept. (Dr. Shankaraiah)

Coordinators of Time-table committee (Dr. UBM, KMP, YSD & CBN)

Principal (Dr. T. N. Nagabhushan)

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ACADEMIC YEAR 2018-19 (EVEN SEMESTER) – PG

Semester: M. Tech **Section: Network and Internet Engineering** **Room. No.: AB203**

Day	7.30 – 8.30	8.30 – 9.30	9.30 – 10.30	10.30 – 11.00	11.00 – 11.50	11.50 – 12.40	12.40 – 1.30	1.30 – 2.30	2.30 – 3.30	3.30 – 4.30	4.30 – 5.30
Mon			LNI220		PGOL1	LNI241 NS	LNI210		LNI27S (Networking lab)		
Tue		LNI210	LNI230		LNI241 MSV	PGOL1	LNI220		PGOL1 (Tutorials)		
Wed		PGOL1	LNI230		LNI210	LNI220 (Tutorials)					
Thu		PGOL1	LNI230		LNI26L (Networking lab)				LNI230L - MNJ (Networking lab)		
Fri			LNI230		LNI210	LNI241 NS	LNI220		LNI210 (Tutorials)		
Sat			LNI241 MSV		LNI220	LNI241 - MNJ (Tutorials)					

BREAK

Sub-Code	Subject Title	Credits	Course Instructor	Initials
LNI210	Mobile Computing	5	B A Sujathakumari	BASK
LNI220	Protocol Engineering	5	B S Renuka	BSR
LNI230	Web Services	5	Dr. N shankaraiah	NS
LNI241	5G technologies	5	Dr. Shankaraiah M S Vinay Prasad	NS + MSV
PGOL1	IOT	5	Thyagarajamurthy	TRM
LNI26L	Networking Lab-2	1.5	Dr. M N Jayaram	MNJ
LNI27S	Design and implementation -2	1.5	Dr. M N Jayaram	MNJ
Total Credits		28		

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION

TIME TABLE FOR ACADEMIC YEAR 2018-19 (EVEN SEMESTER) – PG

Semester: M. Tech **Section: Automotive Electronics** **Room. No.: AB106**

Day	7.30 – 8.30	8.30 – 9.30	9.30 – 10.30	10.30 – 11.00	11.00 – 11.50	11.50 – 12.40	12.40 – 1.30	1.30 – 2.30	2.30 – 3.30	3.30 – 4.30	4.30 – 5.30
Mon			MAL210		MAL220	PGOL2	MAL243		MAL27S (Power Electronics Lab)		
Tue			PGOL2		MAL210	MAL230	MAL243		MAL210 (Tutorials)		
Wed				BREAK	MAL243	MAL210	MAL230	BREAK	MAL26L (CAED Lab)		
Thu			PGOL2		MAL220	MAL243	MAL210		MAL230	MAL243T (Tutorials)	
Fri			MAL230		MAL220	MAL230L (Automotive Electronics Lab)			PGOL2 (Tutorials)		
Sat			PGOL2		MAL220	MAL220 (Tutorials)					

Sub-Code	Subject Title	Credits	Course Instructor	Initials
MAL210	Hybrid Electric Vehicles (HEV's)	5	Halesh M R	HMR
MAL220	Vehicle Engineering	5	Dr. Prasad P (Mechanical)	PP
MAL230	Automotive Instrumentation	5	N Shivaprasad	NSP
MAL243	Automotive Electrical & Electronic system	5	Yashwanth S D	YSD
PGOL2	Solar Energy Systems	5	Dr. N M Renukappa	NMR
MAL26L	Computer Aided Design Lab	1.5	Srinidhi R (Mechanical)	SR
MAL27S	Design and implementation -2	1.5	Dr. N M Renukappa	NMR
Total Credits		28		

Head of the Dept. (Dr. Shankaraiah)

Coordinators of Time-table committee (Dr. UBM, KMP, YSD & CBN)

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